Energy-Efficient Communication Processors: Design and Implementation for Emerging Wireless Systems

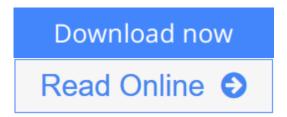*By Robert Fasthuber, Francky Catthoor, Praveen Raghavan, Frederik Naessens*

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This book describes a new design approach for energy-efficient, Domain-Specific Instruction set Processor (DSIP) architectures for the wireless baseband domain. The innovative techniques presented enable co-design of algorithms, architectures and technology, for efficient implementation of the most advanced technologies. To demonstrate the feasibility of the author's design approach, case studies are included for crucial functionality of advanced wireless systems with increased computational performance, flexibility and reusability. Designers using this approach will benefit from reduced development/product costs and greater scalability to future process technology nodes.

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Editorial Review

From the Back Cover

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- Describes a DSIP architecture explicitly for the wireless domain, significantly more efficient than methods commonly in use;
- Includes an efficient DSIP architecture template, which can be reused for specific designs;
- Uses holistic design approach, considering all relevant requirements and combining many innovative/disruptive design concepts;
- Enables design portability, given changing target devices.

Users Review

From reader reviews:

Shelia Coggins:

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Cassie Merritt:

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